6.3 GHz Compact USB Real-Time Spectrum Analyzer

SAN-60 M2

Product Brochure V0.6

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- Outstanding device performance and price advantage
- 9 kHz~6.3 GHz real-time spectrum analyzer
- Integrated 100 kHz-6.3 GHz analog signal generator (opt.)
- 25 MHz analysis bandwidth, 100 GHz/s sweep speed, FPGA signal processing
- IGHz phase noise: -110 dBc/Hz@10 kHz
- Equipped with preamplifier, 1GHz DANL: -162 dBm/Hz
- Core module supported, light as 168 g, 142x54x16 mm, power consumption 7~10 W
- Highly compatible API interfaces (HTRA API) and SAStudio4 GUI
- Compatible with ARM and x86 processors, Linux and Windows operating systems
- Operating temperatures range from 20 °C/- 40 °C to 65 °C (opt.)
- Built-in OCXO (option), temperature drift≤0.15 ppm
- USB3.0/2.0 Type-C interface



Indicator test basis Ha	rdware Version: R5	API: 0.55.12	FPGA: 0.55.2	MCU: 0.55.9	SAS4: 1.55.57			
	Tarra Version. 183	71. 17. 0.00.12						
Frequency								
Frequency Range		9 kHz~6.3 GHz						
Initial Frequency Accurac			nanual correction					
Reference Clock		Internal or external, program-controlled switching, internal TCXO aging<1 ppm/year						
	temperature	temperature drift≤1 ppm; internal OCXO (option), temperature drift≤0.15 ppm						
Disciplined GNSS	Support exte	Support external GNSS (option) disciplines and recalculates the built-in reference clock						
Spectrum Purity								
SSB Phase Noise			dBc/F	lz	-			
Carrier Frequency	500 M	Hz	1 GHz	3 GHz	6 GHz			
1 kHz	-110.3		-105.0	-97.5	-91.2			
10 kHz	-118.		-110.4	-101.2	-99.3			
100 kHz	-118.		-110.5	-100.1	-97.4			
1 MHz Residual Response	-132.		-130.1	-125.5	-120.2			
Spurious Rejection on	Frequency	_	R.L.=0 dBm	R.L.=-20 dBm	R.L.=-50 dBm			
dBm	100 kHz~10		-90	-100	-125			
RBW =1 kHz	100 MHz~6	.3 GHz	-90	-98	-110			
Residual Response Spurious Rejection off	100 kHz~10	00 MHz	-75	-95	-115			
dBm; RBW =1 kHz	100 MHz~6	.3 GHz	-85	-95	-108			
Image Frequency Suppression	>80 dBc (spur	>80 dBc (spurious rejection on), >35 dBc (spurious rejection off, typical value)						
Local Oscillator Related Spurious	<-60 dBc (Offs	<-60 dBc (Offset Center Frequency +/- (N/M)*125 MHz, N/M = 1,2,3,4,5)						
Signal Processing								
Analysis Bandwidth	Maximum 25	Maximum 25 MHz,Decimate Factor:1						
IQ Data	31.25 MSPS Decimate fact	31.25 MSPS Decimate factor: 1,2,4,8,16,32,64,128,256 supported (FPGA)						
	The built-in i	The built-in memory depth is 128 Mbytes						
Storage Depth	' '	Supports continuous and uninterrupted storage when the data generation rate is less that the bus bandwidth, and the storage depth is only limited by the hard disk capacity						
External trigger response		Maximum frequency response 500 times/sec						
Analog IF output	Not supporte	Not supported						
Amplitude	<u>.</u>							
Maximum safe input power	ver 26 dBm	26 dBm 30 MHz~6.3 GHz the preamplifier off (R.L. ≥ 0 dBm)						
(CW)	10 dBm	10 dBm 100 kHz~30 MHz or preamplifier on (R.L. <0 dBm)						
Maximum DC Voltage	+/-15 VDC	+/-15 VDC						
Display Range	DANL~26 dBn	DANL~26 dBm						
Amplitude Accuracy	+/- 1.5 dB	+/- 1.5 dB						
IF in-band spectrum rippl	le +/- 1.75 dB(+/- 1.75 dB(25 MHz analog IF bandwidth)						
Reference level (R.L.)	-50 dBm~23 d	lBm						
RF Preamplifiers	set as autom	atically turn on	or forcibly turn off					
	<1.7:1	<1.7:1 30 MHz~6.3 GHz (R.L. ≥ 10 dBm)						
VSWR	<2.0:1							
	· · · · · · · · · · · · · · · · · · ·	<2.5:1 30 MHz~6.3 GHz (R.L. ≥ -40 dBm)						

Display Average Noise Level	Frequency Range (.L.= 0 dBn iainGrade =			20 dBm irade = 3)	R.L.=-50 dBm (IFGainGrade = 3)	
(DANL) dBm/Hz	9 kHz			-120		-1	30	-145	
RBW=10 kHz	100 kHz			-130		-1	37	-147	
RMS detector	100 MHz~3.0 GHz			-127		-1	42	-158	
	3.0 GHz~6.3 GHz			-126	-		139	-156	
Standard Spectrum Analysis			•		•				
Detector	Positive peak,	Negative	peak, Saı	mpling, A	verage, RMS,	, Max Po	ower		
RBW	0.1 Hz~2.5 MHz								
VBW	0.1 Hz~10 MHz								
Trace Function	Sample, Positive Peak, Negative Peak, Local average, Maximum hold, Minimum hold, Average								
Data Chart	SAStudio4 software provides regular spectrum, waterfall chart, and historical trace								
Sweep speed - Standard Spectrum Analysis 100MHz-6.3GHz	>100 GHz/s FPGA RBW≥250 kHz, B-Nuttall window, spurious rejection: Standard								
Detection Analysis/Zero Span			•						
Highest Time Resolution	32 ns								
Maximum Analysis Bandwidth	25 MHz								
Detection	Positive peak, Negative peak, Sampling, Average, RMS, Max Power								
Real Time Spectrum Analysis									
FFT Analysis	Variable point FFT engine implemented by FPGA. Frame rate compression and trace detection are supported. There is strictly no gap and overlap between FFT frames.								
	FFT refresh rate=10 ^ 9 ns/(N * D * 32 ns); POI = 2*N*D*32 ns N is the number of FFT points (2048,1024,512,256,128,64,32), and D is the decimate factor (1, 2, 4, 8)								
	Typical Settings			FI	FFT Refresh Rate			POI	
	N =2048, D = 1			15	15,258 times/sec			131.072 us	
	N = 32, D = 1 9:			976	76,563 times/sec			2.048 us	
Real-time Analysis Bandwidth	25 MHz								
Window Function	B-Nuttall, FlatTop								
RBW	3.68 MHz-3.59 kHz (FlatTop window); 1.95 MHz~1.90 kHz (B-Nuttall), 11 grades for each window type								
Amplitude Resolution	0.75 dB								
Signal generator (option)									
Frequency range	100 kHz~6.3 GHz, 10 Hz for each step								
Power range	-50 dBm~0 dBm, 0.25 dB for each step								
VSWR	<2.0:1 30 MHz~6.3 GHz								
Non-harmonic spurs	<-50 dBc								
Harmonic wave	100kHz~30M	Hz 3	30MHz~1.	6GHz	1.6GHz~30	GHz	3GHz~3.2GHz	3GHz~6.3GHz	
Second harmonic	<-10 dBc		<-10 dl	Вс	<-20 dB	с	<-20 dBc	<-20 dBc	
Third harmonic and above	<-10 dBc		<-10 dl	Вс	<-20 dB	с	<-20 dBc	<-20 dBc	
	100 kHz~30 MHz				>90 dBc				
	30 MHz~3 GHz				>80 dBc				
Signal leakage to receiver	30 MHz~3 GHz	Z			>80 dBc				

General					
	Power Supply	Type-C (1), dedicated power supply port, please provide 5 V 2 A peal power supply capacity Allowable voltage range: 4.75~5.25 V, ripple less than 200 mVpp			
	Data	Type-C (2), USB3.0 (USB2.0 available but bandwidth limited)			
	RF input	SMA (F), Input impedance 50 Ω			
Input And Output	External reference clock input	MCX (F), amplitude ≥ 1.5 Vpp, input impedance 330 Ω			
	External reference clock output	Not supported			
	External trigger input	Integrated in MUXIO (Type-C), 3.3 V CMOS, input: high impedance			
	External trigger output	Integrated in MUXIO, 3.3 V CMOS			
	Analog IF output	Not supported			
Power Consumption	Peak: 10 W, typical: 7 W~10 W, power port (5V 2A Max), data port (5V 1A Max)				
Operating Temperature	0~50 °C/0~70 °C (Standard temperature class)				
(ambient temperature	-20~65 °C/-20~85 °C (Extended Temperature Class Option) (plastic enclosure and fan not included)				
/device core temperature)	-40~65 °C/-40~85 °C (Wide Temperature Class Option) (plastic enclosure and fan not included)				
Storage Temperature	-20~70 °C (Standard temperature class)				
(ambient temperature)	$-40^{\sim}85$ °C (Extended temperature class and wide temperature options) (plastic enclosure and fan not included)				
Weight and size	Size: 142x54x16 mm, weight:168 g (Excluding protective case and structural fittings, including connector length) Size: 156x62x22 mm, weight:296 g (Including protective case and structural fittings, including connector length)				
Accessories	Flash disk×1, USB cable×2, Power adaptor×1				

^{*}The typical values of the indicators are applicable for the following conditions: (1) Start up and warm up for 20 minutes; (2) Ambient temperature 25 °C (device core temperature 50 °C); (3) Standard sweep mode-Spurious rejection on; (4) 25 MHz analysis bandwidth and IFGainGrade=3; (5) The user shall provide the necessary heat dissipation conditions to ensure that the ambient temperature and the core temperature of the equipment are within the rated range at the same time.

Code	Option	Explanation
01	Built-in OCXO reference clock (hardware opt.)	Providing a reference clock with better stability than the standard configuration, with a temperature drift of<0.15 ppm, increasing the overall power consumption by 0.8 W.
02	Built-in analog signal generator	100 kHz-6.3 GHz signal generator
10	IO extension board (accessory)	Converting the MUXIO interface into multiple MMCX and board to wire connector to facilitate the connection of trigger input, output, and other signals.
11	External GNSS (accessory)	Standard GNSS module connected to MUXIO.
12	External high precision GNSS (accessory)	High precision GNSS module connected to MUXIO.
13	External GNSS disciplined OCXO reference clock (accessory)	Providing GNSS disciplined reference clock and 1PPS, increasing the overall power consumption by 1.1W.
20	Extended temperature class (hardware opt.)	- 20~65 °C/- 20~85 °C(Extended temperature class opt.)
21	Wide temperature class (hardware opt.)	- 40~65 °C/- 40~85 °C(Wide temperature class opt.)

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SAN-60 M2 Product Brochure

